

2001-03

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FPGA

18k~20k

Verilog HDL

Vivado Quartus II ISE

Modelsim

Xilinx Altera FPGA

ZYNQ 7000

Cyclone IV

Spartan6

UART IIC SPI Ethernet DDR

AXI

PCIE

AD/DA

AD7606 MAX186

OV5640 OV7725

Altium Designer

PCB

LAYOUT

Keil

C

FPGA

FPGA

FPGA

1 FPGA  
2 IIC SPI UART  
3 AD  
4 DDR3 AXI  
5

1  
2 AD  
3 RAM  
4 XDMA IP PCIE  
5 FDMA IP DMA AXI Block RAM

1 IIC  
2 SCCB OV5640 RGB565  
3  
4 AXI\_HP DDR3  
5 LCD

1  
2 DDR3 AXI  
3 sobel  
4 RAM  
5 RS485